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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/760,503	01/21/2004	Taketo Fukuro	OKI. 609	1486	
20987 75	590 05/19/2005	9/2005 EXAMINER			
VOLENTINE	FRANCOS, & WHI	ERDEM, FAZLI			
ONE FREEDO	M SQUARE				
11951 FREEDOM DRIVE SUITE 1260			ART UNIT	PAPER NUMBER	
RESTON, VA	20190		2826	"	

DATE MAILED: 05/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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·		10/760,503	FUKURO, TAK	(ETO
Office Action	Summary	Examiner	Art Unit	
		Fazli Erdem	2826	
The MAILING DATE Period for Reply	of this communication	on appears on the cover	sheet with the correspondence	address
 If NO period for reply is specified a Failure to reply within the set or ex 	HIS COMMUNICAT e under the provisions of 37 illing date of this communicat ve is less than thirty (30) day bove, the maximum statutory ended period for reply will, b er than three months after th	TION. CFR 1.136(a). In no event, hower tion. s, a reply within the statutory mini period will apply and will expire Systatute, cause the application to	_ , ,	is communication.
Status				
1) Responsive to comm	nunication(s) filed or	08 February 2005.		
2a) This action is FINAL	. 2b)	This action is non-fina	d.	
		·	mal matters, prosecution as to	the merits is
closed in accordanc	e with the practice u	nder <i>Ex part</i> e Quayle, 1	935 C.D. 11, 453 O.G. 213.	
Disposition of Claims				
5) Claim(s) <u>1-7,9 and 1</u>	m(s) is/are w <u>0</u> is/are allowed.	ithdrawn from considera	ution.	
6)⊠ Claim(s) <u>11-14 and</u> 7)⊠ Claim(s) <u>15 and 21</u> i 8)□ Claim(s) are	s/are objected to.		ment.	
Application Papers	·	·		
Replacement drawing	on is/are: a)[lest that any objection sheet(s) including the	☐ accepted or b)☐ objecto the drawing(s) be held incorrection is required if the	ected to by the Examiner. in abeyance. See 37 CFR 1.85(a drawing(s) is objected to. See 37 attached Office Action or form	7 CFR 1.121(d).
Priority under 35 U.S.C. § 11	9			
12) △ Acknowledgment is r a) △ All b) □ Some * 1. △ Certified copie 2. □ Certified copie 3. □ Copies of the application fro	nade of a claim for foct) None of: s of the priority docuse of the priority docusertified copies of the mathe International E	uments have been recei uments have been recei	ived. ived in Application No ve been received in this Nation (a)).	nal Stage
Attachment(s)		_		
Notice of References Cited (PT 2) Notice of Draftsperson's Patent 3) Information Disclosure Stateme Paper No(s)/Mail Date	Drawing Review (PTO-9	48) SB/08) 5) <u>□</u> 1	Interview Summary (PTO-413) Paper No(s)/Mail Date Notice of Informal Patent Application (Other:	PTO-152)

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-7, 9 and 10 allowed
- 2. Claims 15 and 21 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 11-14 and 16-20 rejected under 35 U.S.C. 103(a) as being unpatentable over Arase (JP 04069939) in view of Williams (5,998,837).

Regarding Claims 11-14 and 16-20, Arase discloses field effect transistor of insulating gate type where in Figl 1, an N+ type diffusion region 13 is disclose in the P-type substrate 11 with P+ type polysilicon plug 17a is located on top of the N+ type diffusion region and a wiring layer 18 is located on top of the P+ type polysilicon plug 17a.

Futhermore, N+ type diffusion region 13 extends on the surface of the P-type substrate 11 and P+ type polysilicon plug extends from the surface of the substrate. Region 17a and 13 form a PN junction. Wiring layer 18 a is directly on top of the polysilicon plug 17a.

Arase fails to disclose the required doping densities. However, Williams discloses a

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trench-gated power MOSFET with protective diode having adjustable breakdown voltage

where in Claim 14 the required doping density is disclosed.

It would have been obvious to one of having ordinary skill in the art at the time

the invention was made to include the required doping density in Arase as taught by

Williams in order to have a semiconductor device with increase reliability.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Fazli Erdem whose telephone number is (703) 305-3868. The

examiner can normally be reached on M - F 8:00 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan Flynn can be reached on (703) 308-6601. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

FE May 13, 2005 NATHAN J. FLYNN SUPERVISORY PATENT EXAMINER FECHNOLOGY CENTER SOME

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